

327	108
Class	Subclass

ISSUE CLASSIFICATION

PATENT NUMBER

CPA

O.I.P.E.

PATENT DATE

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APPLICATION NO. 09/811456	CONT/PRIOR D F	CLASS 327	SUBCLASS 108	ART UNIT 2816	EXAMINER Long Nguyen
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TITLE > APPLICANTS

Masahito Isoda

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Input buffer circuit for semiconductor device

PTO-2040
12/09[illegible]☐ Continued on Issue Slip Inside File Jacket

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DRAWINGS			CLAIMS ALLOWED	
Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<div>_____ (Assistant Examiner) _____ (Date)</div> <div>_____ (Primary Examiner) _____ (Date)</div> <div>_____ (Legal Instruments Examiner) _____ (Date)</div>			NOTICE OF ALLOWANCE MAILED	
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